

ABSTRACT

There is provided an information processing method characterized in that, in accordance with an instruction
5 from a host CPU 411, either CPU 103 or 104 loads a common code and an instruction code defined to be executed by itself from an external memory 110 into an internal memory 101, the other CPU loads an instruction code defined to be executed by itself from the external memory
10 110 into the internal memory 101, then the respective CPUs load the respective instruction codes defined to be executed by themselves that are loaded in the internal memory 101, and executing the common code that is loaded in the internal memory 101 as required.